L Number	Hits	Search Text	DB	Time stamp
1	1655337	display\$1	USPAT;	2004/09/01 13:00
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
2	1414614	CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1	USPAT;	2004/09/01 13:02
			US-PGPUB;	2001,03,02 20.02
			EPO; JPO;	
			DERWENT;	
	040074		IBM_TDB	0004/00/01 10 00
3	310071	display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1	USPAT;	2004/09/01 13:03
		FED\$1)	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	
4	2609	(display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1	USPAT;	2004/09/01 13:29
		FED\$1)) & ((plurality near3 pixel\$1) with	US-PGPUB;	
		matrix)	EPO; JPO;	
			DERWENT;	
5	2288617	switch\$3	USPAT;	2004/09/01 13:04
			US-PGPUB;	
			EPO; JPO;	-
			DERWENT;	
	2016	(())	IBM_TDB	
6	2046	((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with	USPAT; US-PGPUB;	2004/09/01 13:05
		matrix)) & switch\$3	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
7	2014		USPAT;	2004/09/01 13:07
		FED\$1)) & ((plurality near3 pixel\$1) with	US-PGPUB;	
		matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)	EPO; JPO; DERWENT;	
		Scant Gaca gace	IBM TDB	
8	92	(capacitor\$1 with (connect\$3 coupl\$3) with	USPAT;	2004/09/01 13:11
		between with switch\$3) & (((display\$1 &	US-PGPUB;	
		(CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) &	EPO; JPO;	
		((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data	DERWENT; IBM TDB	
		gate))		
9	865	aging near3 circuit\$3	USPAT;	2004/09/01 13:10
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
10	1	((capacitor\$1 with (connect\$3 coupl\$3)	USPAT;	2004/09/01 13:10
	_	with between with switch\$3) &	US-PGPUB;	2001,00,01 10.10
		(((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1	EPO; JPO;	
]		FED\$1)) & ((plurality near3 pixel\$1) with	DERWENT;	
		matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (aging near3	IBM_TDB	
		circuit\$3)		
11	293		USPAT;	2004/09/01 13:11
		near3 circuit\$3)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
12	5	((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1	<pre>IBM_TDB USPAT;</pre>	2004/09/01 13:11
		FED\$1)) & ((plurality near3 pixel\$1) with	US-PGPUB;	2001/02/01 ID.II
		matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1	EPO; JPO;	
		FED\$1) & (aging near3 circuit\$3))	DERWENT;	
13	5	ewitch\$3 & (((dienlau\$1 c /CDM\$1 OTED\$1	IBM_TDB	2004/00/01 12:11
10	S	switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3	USPAT; US-PGPUB;	2004/09/01 13:11
		pixel\$1) with matrix)) & ((CRT\$1 OLED\$1	EPO; JPO;	
		LCD\$1 PDP\$1 FED\$1) & (aging near3	DERWENT;	
		circuit\$3)))	IBM_TDB	

Page 1

15	2	<pre>(capacitor\$1 same (connect\$3 coupl\$3) with between with switch\$3) & ((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:13
16	2	between with switch\$3) & (((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:17
17	2	circuit\$3)))) "20040108518"	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/01 13:17
18	1	"20040108518" & aging	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/01 13:18
14	5	<pre>((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3))))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:26
19	2	<pre>(((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)))) & prevent\$3</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:27
20	1	"20040108518" & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/01 13:28
21	156	((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/01 13:32
22	6	(((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)) & prevent\$3) & (plurality near7 pixel\$1)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/01 13:29
23	74	<pre>((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & prevent\$3</pre>	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:14

			T.C.D.T.	10004/00/01 12 22
24	79	<pre>((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 with pixel\$1)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:33
25	75	<pre>((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:33
26	63		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:37
27	27		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:44
28	2	<pre>(((((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & prevent\$3) & organic) & (AC near7 voltag\$3)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:46
29	13		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:46
30	13		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:28
31	2	<pre>(((((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & ((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & (AC near7 voltag\$3)) & prevent\$3) & organic</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:29

Page 3